

# Position Paper: Challenges Towards Securing Hardware-assisted Execution Environments

Zhenyu Ning<sup>1</sup> **Fengwei Zhang**<sup>1</sup> Weisong Shi<sup>1</sup> Larry Shi<sup>2</sup>

<sup>1</sup>Wayne State University

<sup>2</sup>University of Houston

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# Overview of The Talk



- ▶ Motivation
- ▶ TEE Background
- ▶ Research Challenges
- ▶ Conclusion

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Hardware-assisted Trusted Execution Environments (TEEs) have been widely adopted in commodity systems

Examples of TEE include but not limited to:

- ▶ Intel software guard extensions (SGX) [1, 2, 3]
- ▶ AMD memory encryption technologies [4, 5, 6]
- ▶ ARM TrustZone [7]
- ▶ x86 system management mode (SMM) [8]
- ▶ Intel management engine (ME) [9]
- ▶ AMD platform security processor (PSP) [10]



Due to the nice features provided by the TEEs, the code base in a TEE is getting large

- ▶ OS running in TrustZone [11]
- ▶ A hypervisor is deployed in SMM [12]
- ▶ Linux containers running in SGX [13]

The complexity of the software introduces vulnerabilities, and  
"Trusted" execution environments are not trustworthy



Trusted Execution Environments have been introduced in different platforms for securing software execution, but achieving security not only depends on technologies of execution environments themselves (e.g., small TCB, strong isolation), but also relies on the **executed code**.

State-of-the-art trusted execution environments research lacks:

- ▶ Frameworks to verify the executed code
- ▶ Defenses within the trusted environments
- ▶ Methods detecting compromised TEEs
- ▶ Mitigation plans for TEE-attack responses

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We category TEEs into three types:

- ▶ Ring 3 TEEs implemented via memory encryption
  - ▶ Intel SGX
  - ▶ AMD memory encryption technologies
- ▶ Ring -2 TEEs implemented via memory restriction:
  - ▶ x86 SMM
  - ▶ ARM TrustZone
- ▶ Ring -3 TEEs implemented via co-processors:
  - ▶ Intel ME
  - ▶ AMD PSP





## Intel SGX

- ▶ Was introduced at HASP 2013
- ▶ A set of instructions and mechanisms for memory accesses

## AMD memory encryption technologies

- ▶ Was introduced at ISCA 2016
- ▶ Two new features: Secure Memory Encryption (SME) and Secure Encrypted Virtualization (SEV)

## Intel SGX v.s. AMD SME/SEV

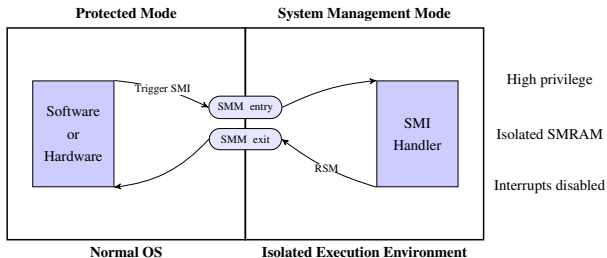
- ▶ SGX is at user-level but SEV can encrypt VMs
- ▶ Encryption performance comparison

# Background: Ring -2 TEEs via Memory Restriction



## x86 System Management Mode

- ▶ A special CPU on x86
- ▶ Isolated System Management RAM (SMRAM) that is inaccessible from OS
- ▶ Only way to enter SMM is to trigger a System Management Interrupt (SMI)
- ▶ Executing RSM instruction to resume OS (Protected Mode)

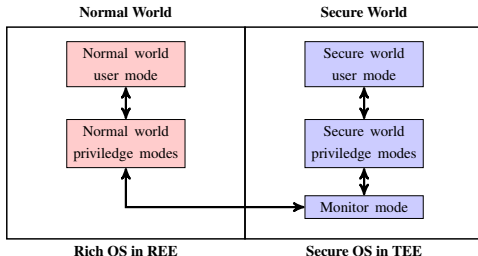


# Background: Ring -2 TEEs via Memory Restriction



## ARM TrustZone Technology

- ▶ Similar to SMM on x86
- ▶ Two worlds: secure mode and normal mode
- ▶ Secure Monitor Call (SMC) instruction or triggering secure interrupts to switch to secure mode

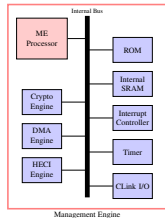


## Intel Management Engine

- ▶ Micro-computer embedded inside of all recent Intel processors
- ▶ First ME application: Active Management Technology (AMT)
- ▶ Others including enhanced privacy identification, protected audio video path, identity protection technology, and boot guard.

## AMD Platform Security Processor

- ▶ Similar technology to Intel ME
- ▶ A co-processor on AMD CPUs



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This paper considers the following **Research Challenges (RCs)** for further securing trusted execution environments

- ▶ Hunting Bugs in TEEs code
- ▶ Protecting Mechanisms in the TEEs
- ▶ Detecting a Compromised TEE
- ▶ Patching and Rejuvenation of TEEs

- ▶ The software running in a TEE can contain text-book vulnerabilities such as buffer overflows since this software can be written by careless programmers or third-party developers
  - ▶ "Millions of BIOSes" are easy to be compromised because the known vulnerabilities of SMM code are never patched in the BIOS [14]
  - ▶ Vulnerabilities that can be used to execute arbitrarily code in TrustZone [15]
  - ▶ A series of SGX-based applications have been developed such as SCONE [13], Haven [16], VC3 [17], etc.
- ▶ Existing solutions of bug hunting can not be applied directly because the TEEs code requires particular environments
- ▶ Open source code v.s. binary images (SMM, TrustZone, and ME)



**RC1 failed.** It is impractical to have perfect code running in the TEEs, and the attackers will eventually find a vulnerability and exploit it at some point; however, existing TEEs lack of defense mechanisms in the execution environments

- ▶ The code running in SMM shares a single address space and paging is disabled
- ▶ Applications running in the SGX enclaves do not have protection mechanisms such as ASLR
- ▶ Defense mechanisms such as non-executable stack, data execution prevention, and address space randomization are missing in TEEs

Since we consider these environments are more secure than the normal OS, these basic system defense mechanisms are needed for securing the environment



## RC3: Detecting a Compromised TEE



**RC1&2 failed.** A TEE has been compromised due to the vulnerabilities in the code. How to detect a compromised TEE? TEEs run at a high-privilege memory space that is inaccessible from the system software or use encrypted memory that their contents are mysterious without the key

- ▶ Intel SGX encrypts its code and data in enclaves
- ▶ SMM and TrustZone code is not accessible by the system software (e.g., OS)

Because of these "security protection" features, a TEE can achieve a strong security guarantee. However, after compromising a TEE, attackers can implement undetectable advanced rootkits in it

- ▶ SMM keyloggers [18] and SMM rootkits developed by NSA [19]
- ▶ Ring -3 rootkits [20]



**RC1&2 failed and RC3 succeed.** We know that a TEE has been compromised. How to mitigate attackers from a compromised TEE and patch it to a good state?

- ▶ Using System software to update the compromised TEE. How about ring -2 TEEs (SMM and TrustZone) and ring -3 TEEs (Intel ME)?
- ▶ If the TEE is compromised, it is likely that the system software is malicious as well
- ▶ Using a TEE to update another TEE (Intel ME to patch BIOS/SMM)?

Ensuring the restore process is secure, we need a **Trust Base**

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Existing trusted execution environments focus on removing trust from system software and achieving strong isolation by leveraging hardware support. We present four research challenges towards securing TEEs

- ▶ Hunting Bugs in TEEs code
- ▶ Protecting Mechanisms in the TEEs
- ▶ Detecting a Compromised TEE
- ▶ Patching and Rejuvenation of TEEs

The goal of this position paper is to draw the attention to the system security community about the challenges for achieving a more secure execution environment

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Thank you!



Email: [fengwei@wayne.edu](mailto:fengwei@wayne.edu)

Homepage: <http://fengwei.me>

Questions?